

H. Montgomery Manning et al.

Title:

Semiconductor Constructions, and Methods of Forming Capacitor

Devices

Assignee:

Micron Technology, Inc.

Serial No.:

10/733,181

Filed:

December 10, 2003

INFORMATION DISCLOSURE STATEMENT

PURSUANT TO 37 C.F.R. §§ 1.56, 1.97 AND 1.98

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, the Examiner's attention is directed to the reference listed on the attached Form PTO-1449 and a copy of which is attached. No admission is made regarding whether the submitted reference is prior art.

This Information Disclosure Statement is being filed within three months of the filing date of the application or before the mailing of a first Office Action on the merits, whichever occurs last. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this Information Disclosure Statement, please charge the fee specified under 37 C.F.R. § 1.17(p) to Deposit Account No. 23-0925.

Citation of this patent is respectfully requested.

Respectfully submitted,

Date:

Attorney:

David G. Latwesen, Ph.D.

Reg. No. 38,533

Wells St. John P.S.

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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.										